Flattening Statecharts without Explosions

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Outline

- Introduction and Motivation
  - Hierarchical Statecharts
  - Flat Statecharts
  - The Flattening problem
- A Lower Bound for Flattening
- Polynomial flattening for Code Generation
- Experimental results
- Conclusion
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• **Introduction and Motivation**
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• **Conclusion**
Hierarchical Statecharts

- Entry & exit actions on every state.
- Transitions guarded by events and current configuration.
- Signals (local events), signal queue.
- Concurrent, but usually compiled to sequential code.
Hierarchical Statecharts (IV)
Hierarchical Statecharts
Flat Statecharts

- Set of Mealy Machines
- No entry/exit actions in states
- Synchronization on states
- Configurations = state vectors

- All machines active all the time
- Hier. statechart with history
- Flat transition scopes
- Nondeterministic order
Flat Statecharts

\[\text{case } e \text{ of} \]

\begin{align*}
\text{alarm} : & \text{if idle then } \sigma'[0] := \text{armed}; \\
& \text{if idle then } \sigma'[0] := \text{idle}; \\
& \text{if fired then } \sigma'[0] := \text{armed}; \text{beepOff}; \\
& \text{if fired} \land \text{night} \land \text{on then } \sigma'[1] := \text{glowing}; \text{glow}; \\
& \text{if fired} \land \text{day} \land \text{on then } \sigma'[1] := \text{off}; \text{lightOff}; \\
\text{alarmTO} : & \text{if armed then } \sigma'[0] := \text{fired}; \text{beepOn}; \\
& \text{if armed} \land \text{off then } \sigma'[1] := \text{on}; \text{lightOn}; \\
& \text{if armed} \land \text{glowing then } \sigma'[1] := \text{on}; \text{lightOn}; \\
\text{snooze} : & \text{if fired then } \sigma'[0] := \text{delayed}; \text{beepOff} \ldots \\
\text{snoozeTO} : & \text{if delayed then } \sigma'[0] := \text{fired}; \text{beepOn} \ldots \\
\text{dark} : & \text{if off then } \sigma'[1] := \text{glowing}; \text{glow} \ldots \\
\text{bright} : & \text{if glowing then } \sigma'[1] := \text{off}; \text{lightOff} \ldots
\end{align*}
Flattening = Hierarchy Elimination

- Unfortunately becomes complex in presence of exit actions.
- What are the exit actions executed by top transition?
Semantics: an I/O Alternating Labeled Transition System.

[Simulation] Statechart configuration $\sigma_0$ simulates a state configuration $\sigma'_0$, written $\sigma'_0 \preceq \sigma_0$, iff whenever $\sigma'_0 \xrightarrow{e \ os} \sigma'_1$ then also $\sigma_0 \xrightarrow{e \ os} \sigma_1$ and $\sigma'_1 \preceq \sigma_1$.

Note! input-enabled setting and non-determinism.

[Flattening] Let $F$ be an algorithm transforming statecharts. $F$ is a flattening algorithm if for any hierarchical $S$ it yields a flat $S'$ such that $S' \preceq S$. 
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A Lower Bound on Flattening

[Theorem]
There exists a hierarchical statechart $S$ such that for any flat statechart $S'$ implementing it, $S' \preceq S$, such that $S'$ does not use signals: the size of $S'$ is in $\Omega(2^{\sqrt{s}})$, where $s$ represents the size of $S$.

[Wasowski, SFEDL’04]
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Non-explosive Flattening

**Theorem** For any hierarchical UML statechart $S$ there exists a flat statechart $S'$ with queue-based signal communication such that $S' \preceq S$ and the size of $S'$ is at most polynomial in the size of $S$.

A signal queue in the output language, or the sequence of rule execution, can be used to achieve flattening in polynomial space.

This is the approach of SCOPE code generator.
Flatten the state tree first

- Hierarchy represented explicitly in guards: substitute each occurrence of state $s$ with a conjunction of $s$ and its ancestors.
- Clearly no explosion here.
Flatten hierarchical transitions

$[e_1 : (D_{12} \land \neg E_{11})]/\langle o_1, s_1 \rangle$

$[e_2]/\langle o_2 \rangle$

$[s_1 : (D_{12})]/\langle o_2 \rangle$
At most as many new transitions as the number of states.
Flatten hierarchical transitions (III)

- Add a scheduling transition:
  \[ t'_1 = D_1 \frac{[e_1 : (D_{12} \land \neg E_{11})]/\langle o_1, s_1 \rangle}{[s_1 : (\neg C_2)]/\langle o_2 \rangle} \]

- Note that this only works due to weak conformance requirement (simulation). Only one possible sequence is chosen.

- In simple-minded translation more exit signals are added.
What comes out of it?
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Experimental results

- Code size compiled for AVR using avr-gcc 3.3.2
- Bare model and runtime, dummy actions, dummy event feeder.
- Ratio varies from 0.6 to 0.96.
Execution time on pseudo-random trace of $10^7$ events.
Run on Linux 450MHz Pentium II.
Ratio varies from 0.58 to 1.31.
Conclusion

- A superpolynomial, subexponential lower bound for flattening statecharts in absence of message passing, which indicates the expressive power of signal communication (sequencing).
- If your flattening does not rely on sequencing mechanism it is bound to explode.
- A polynomial algorithm for application in code generation, which is surprising (see at least 7 references for counter intuitions in the LCTES paper).
- An efficient code generator, which significantly improves over the industrial implementation.

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Thank you for Your attention.